INTEGRATED MICRO-DROPLET GENERATOR

FIELD OF THE INVENTION

The present invention generally relates to an integrated micro-droplet generator and more particularly, relates to a thermal bubble type inkjet head that is equipped with a symmetrical, off-shooter heater and a method for fabricating the head.

BACKGROUND OF THE INVENTION

Since the advent of printers, and specifically for low cost printers for personal computers, a variety of inkjet printing mechanisms have been developed and utilized in the industry. These inkjet printing mechanisms include the piezoelectric type, the electrostatic type and the thermal bubble type, etc. After the first thermal inkjet printer becomes commercially available in the early 1980's, there has been a great progress in the development of inkjet printing technology.

In an inkjet printer, a liquid droplet injector is used as one of the key mechanisms. To provide a high-quality and reliable inkjet printer, the availability of a liquid droplet

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injector capable of supplying high-quality droplets at high-frequency and high-spacial resolution is critical.

Presently, there are two types of inkjet printers that 004 are available in the market, the piezoelectric type and the thermal The thermal inkjet system, also known as thermal bubble type. inkjet system, thermally driven bubble system or as bubble jet system utilizes bubble to eject ink droplets out of an ink supply while piezoelectric printers utilize piezoelectric chamber. actuators to pump ink out from a reservoir chamber. The principle of operation for a thermal bubble inkjet system is that an electrical current is first used to heat an electrode to boil When the liquid is in a liquid in an ink reservoir chamber. boiling state, bubble forms in the liquid and expands and thus functioning as a pump to eject a fixed quantity of liquid from the reservoir chamber through an orifice and then forms into droplets. When the electrical current is turned-off, the bubble generated collapses and liquid refills the chamber by capillary force.

When evaluating the performance of a thermal bubble inkjet system, factors such as droplet ejection frequency, cross talk between adjacent chambers and the generation of satellite

droplets are considered. Two of these performance requirements, i.e. the satellite droplets, which degrade the sharpness of the image produced and the cross talk between adjacent chambers and flow channels which decrease the quality and reliability of the inkjet system are frequently encountered. In order to improve the performance of a thermal bubble inkjet system, these drawbacks must be corrected.

It is therefore an object of the present invention to provide a thermal bubble inkjet head that does not have the drawbacks or the shortcomings of the conventional thermal bubble inkjet head.

It is another object of the present invention to provide a thermal bubble inkjet head that is equipped with a symmetrical ring-shaped heater for generating bubbles.

It is another further object of the present invention to provide a thermal bubble inkjet head that is equipped with an ink chamber.

It is yet another object of the present invention to provide a method for fabricating a thermal bubble inkjet head that is equipped with a symmetrical heater.

1010 It is still another further object of the present invention to provide a method for fabricating a thermal bubble inkjet head that is equipped with a symmetrical heater by utilizing two separate thick photoresist deposition processes and a nickel electroplating process.

SUMMARY OF THE INVENTION

In accordance with the present invention, a thermal bubble inkjet head that is equipped with a symmetrical heater and a method for fabricating such head are disclosed.

In a preferred embodiment, a method for fabricating a thermal bubble inkjet head that is equipped with off-shooter heaters is provided which includes the operating steps of providing a silicon substrate that has a top surface and a bottom surface; forming a first and a second insulating material layer of at least 1000Å thick on the top and bottom surfaces; reactive ion etching an opening for a manifold in the second insulating material layer

on the bottom surface; wet etching a funnel-shaped manifold in the silicon substrate; forming a symmetrical ring-shaped heater on the first insulating material layer on the top surface; depositing and patterning an interconnect with a conductive metal in electrical communication with the ring-shaped heater; depositing a third insulating material layer on top of the ring-shaped heater and the first insulating material layer; spin-coating a first photoresist layer of at least 2000Å thick on top of the third insulating material layer; patterning by UV exposure an ink chamber in fluid communication with said manifold; depositing a metal seed layer on the first photoresist layer and patterning an inkjet orifice in the metal seed layer; spin-coating a second photoresist layer of at least 2000Å thick on the metal seed layer and patterning the inkjet orifice; removing the developed second photoresist layer except on top of the inkjet orifice; electroplating nickel on top of the metal seed layer encapsulating the second photoresist layer on top of the inkjet orifice; stripping away the second photoresist layer on top of the inkjet orifice; reactive ion etching away the second insulating material layer on the bottom surface of the silicon substrate and the first insulating material layer exposed in the manifold; and stripping away the first photoresist layer from the ink chamber.

may further include the step of forming the first and second insulating material layers with either SiO_2 or Si_3N_4 , or the step of wet etching a funnel-shaped manifold in the silicon substrate by KOH, or the step of forming the ring-shaped heater with TaAl, or the step of depositing the third insulating material layer of Si_3N_4 or SiC. The method may further include the step of spin-coating a first photoresist layer preferably of at least 5000\AA thick, or the step of depositing the metal seed layer of Cr and Ni, or the step of stripping away the second photoresist layer by a wet etching method, or the step of stripping away the first photoresist layer from the ink chamber by a wet etching technique, or the step of patterning the inkjet orifice in the metal seed layer adjacent to said ring-shaped heater.

The present invention is further directed to a thermal bubble inkjet head that is equipped with symmetrical heaters which includes a silicon substrate that has a top surface and a bottom surface; a first and a second insulating material layer of at least 1000Å thick on the top and bottom surfaces; a funnel-shaped manifold formed in the second insulating material layer and the silicon substrate; a symmetrical ring-shaped heater formed on the

first insulating material layer on the top surface; an interconnect formed of a conductive metal in electrical communication with the ring-shaped heater; a third insulating material layer on top of the ring-shaped heater and the first insulating material layer; a first photoresist layer of at least 2000Å thick on top of the third insulating material layer; an ink chamber formed in the first photoresist layer in fluid communication with the funnel-shaped manifold; a metal seed layer on top of the first photoresist layer and an inkjet orifice formed in the metal seed layer; and a Ni layer on top of the metal seed layer with an aperture formed therein in fluid communication with the inkjet orifice.

In the thermal bubble inkjet head that is equipped with a symmetrical heater, the first photoresist layer preferably has a thickness of at least 5000Å, the inkjet orifice is formed in close proximity to the ring-shaped heater; the first and second insulating material layers may be a $\rm SiO_2$ layer or a $\rm Si_3N_4$ layer. The ring-shaped heater may be formed of TaAl, the metal seed layer may be deposited of Cr or Ni. The ring-shaped heater may be positioned in the ink chamber. The inkjet orifice may be formed in the ink chamber opposite to the ring-shaped heater. The inkjet head may be a monolithic head.

BRIEF DESCRIPTION OF THE DRAWINGS

These and other objects, features and advantages of the present invention will become apparent from the following detailed description and the appended drawings in which:

Figure 1A is an enlarged, cross-sectional view of a present invention silicon substrate coated with an insulating material layer on a top surface and a bottom surface.

Figure 1B is an enlarged, cross-sectional view of the present invention silicon substrate of Figure 1A with an opening dry etched in the bottom insulating layer and a funnel-shaped manifold wet etched in the silicon substrate.

Figure 1C is an enlarged, cross-sectional view of the present invention silicon substrate of Figure 1B with a metal layer deposited on top and then formed into an interconnect.

O020 Figure 1D is an enlarged, cross-sectional view of the present invention silicon substrate of Figure 1C with a heater connected to an interconnect.

Figure 1E is an enlarged, cross-sectional view of the present invention silicon substrate of Figure 1D with a passivation layer deposited on top of the substrate.

Figure 1F is an enlarged, cross-sectional view of the present invention silicon substrate of Figure 1E with a thick photoresist layer deposited on top.

Figure 1G is an enlarged, cross-sectional view of the present invention silicon substrate of Figure 1F with a pattern formed in the photoresist layer by UV exposure.

Figure 1H is an enlarged, cross-sectional view of the present invention silicon substrate of Figure 1G with a metal seed layer deposited and patterned for the inkjet orifice on top.

Figure 1I is an enlarged, cross-sectional view of the present invention silicon substrate of Figure 1H with a second thick photoresist layer spin-coated on top and patterned.

O026 Figure 1J is an enlarged, cross-sectional view of the present invention silicon substrate of Figure 1I with the second photoresist layer developed.

Figure 1K is an enlarged, cross-sectional view of the present invention silicon substrate of Figure 1J with an orifice plate electroplated on top.

Figure 1L is an enlarged, cross-sectional view of the present invention silicon substrate of Figure 1K with the remaining second photoresist layer stripped to form the orifice.

Figure 1M is an enlarged, cross-sectional view of the present invention silicon substrate of Figure 1L with the bottom insulating layer and the top insulating layer and the passivation layer stripped by dry etching.

O030 Figure 1N is an enlarged, cross-sectional view of the present invention silicon substrate of Figure 1M with the first photoresist layer stripped to form the ink chamber.

Figure 2A is an enlarged, cross-sectional view of the present invention inkjet head illustrating its first operating step wherein a ring-shaped bubble is generated by the ring-shaped heater.

Figure 2B is an enlarged, cross-sectional view of the present invention inkjet head illustrating the second step of operation wherein the ring-shaped bubble is enlarged to push out an ink column.

Figure 2C is an enlarged, cross-sectional view of the present invention inkjet head illustrating the third operating step in which the bubble is further enlarged to push out the ink column.

Figure 2D is an enlarged, cross-sectional view of the present invention inkjet head illustrating the fourth operating step in which a circular bubble is generated to dislodge the ink column.

Figure 2E is an enlarged, cross-sectional view of the present invention inkjet head illustrating the circular bubble is collapsed.

Figure 3 is a third embodiment of the present invention thermal bubble inkjet head equipped with two inkjet orifices for two symmetrial, off-shooter heaters.

DETAILED DESCRIPTION OF THE PREFERRED AND ALTERNATE EMBODIMENTS

The present invention discloses a thermal bubble inkjet head that is equipped with a symmetrical heater. The present invention further discloses a method for fabricating such a thermal bubble inkjet head.

In the present invention method, two separate thick photoresist deposition processes by spin-coating and a nickel electroplating process are required for achieving the final structure. The first thick photoresist spin-coating process is used for forming an ink chamber. The second thick photoresist spin-coating process is used to form a mold layer for forming an inkjet orifice. The nickel electroplating process is used to form a top plate on the inkjet head through which the injector orifice is formed. None of these novel processing steps is used in conventional inkjet head formation methods.

The present invention thermal bubble inkjet head has a 0039 construction of the monolithic type formed on a silicon single crystal substrate. A ring-shaped heater electrode is formed in a symmetrical manner for superior liquid droplet generation. The ring-shaped heater electrode is further formed with a high present invention directional perpendicularity. With the symmetrically constructed ring-shaped heater electrode, conventional problems of satellite droplets and interferences between adjacent orifices and flow channels can be minimized. benefits and advantages described above are achieved by the present invention symmetrically arranged heater electrode is formed either in an off-shooter arrangement or in a back-shooter arrangement. An off-shooter arrangement process flow is described below, while the process flow for a back-shooter arrangement can be similarly executed with minor modifications. The term "off-shooter" means the position of the heater off-shifted the position of the nozzle from the normal direction.

Referring initially to Figure 1A, wherein a silicon substrate 10 used for constructing the present invention inkjet head is shown. On a top surface 12 of the silicon substrate, and on a bottom surface 14 of the same, are then deposited by a low

pressure chemical vapor deposition method insulating material layers 16 and 18, respectively. The insulating material layers 16,18 can be formed of either $\mathrm{SiO_2}$ or $\mathrm{Si_3N_4}$ to a thickness between about 1000 Å, and preferably to about 2000 Å. In the preferred embodiment, a P-type 101 mm diameter silicon wafer that has a crystal orientation of (100) is utilized. A RCA cleaning procedure is first used to clean the wafer prior to processing. The $\mathrm{SiO_2}$ layer may also be formed by a wet oxidation method in a furnace tube to a thickness larger than 1 $\mu\mathrm{m}$.

A first mask is then used, as shown in Figure 1B, in a photolithographic process to define the position of manifold 20 and forming the manifold 20 by first dry etching the SiO₂ layer 18 by a reactive ion etching technique, and then etching the silicon layer 22 by a wet etching utilizing KOH solution. The process is completed by rinsing the wafer with DI (deionized) water.

In the next step of the process, shown in Figure 1C, a second mask is first used in a photolithographic process to define the locations of an interconnect 34. A metal layer such as Al or Cu is first evaporated on top of the insulating material layer 16

and patterned into the interconnect 34. The process is again completed with a DI water rinsing of the silicon wafers.

A symmetrical ring-shaped heater electrode 28 is then formed on top of the interconnect 34 by first depositing a metal layer such as TaAl alloy and then photolithographically patterning the metal layer. A third photomask is used for the heater electrode forming process shown in Figure 1D. Following the heater electrode forming process, shown in Figure 1E, an insulating material layer, or a passivation layer 36, is deposited on top of the silicon substrate 10 to provide insulation to the various structures of the interconnection 34 and the heater electrode 28. The passivation layer 36 is a protection layer which can be deposited of a material selected from Si₃N₄, SiC and SiO₂ by a plasma enhanced chemical vapor deposition technique. This is shown in Figure 1E.

The present invention novel method continues by the advantageous deposition step, shown in Figure 1F, of a first thick photoresist layer 38 on top of the silicon substrate 10. The photoresist layer 38 should have a thickness of at least 20 μ m, and preferably 25-35 μ m deposited by a spin-coating technique and then

baked for drying. An exposure process utilizing UV radiation, shown in Figure 1G, follows by using a fourth photomask to define the size and location of the ink chamber 40. A developing step is not executed at this stage such that all the photoresist layers 38, either the exposed portion 44 or the unexposed portion 38, stays on top of the silicon substrate 10. This is a critical step of the present invention and must be patterned with great accuracy such that the positions of the ink chamber 40 can be determined.

In the next step of the process, shown in Figure 1H, a metal seed layer 46 is deposited on top of the photoresist layer 38,44 and patterned to define an injection orifice 48 in the metal seed layer. The metal seed layer may be deposited of a Cr/Ni alloy by sputtering or evaporation and used as a seed layer for a subsequent electroplating process. A fifth photomask is used in a photolithography process to define the size and location of the injection orifice 48. The injection orifice 48 is formed by a wet etching technique followed by a process for removing the photoresist layer used in the lithography process.

The present invention novel method is followed, as shown in Figure 1I, by a second thick photoresist layer 50 deposition process. The deposition can be carried out by a spin-coating technique and then the photoresist layer 50 is patterned for the injection passage 52. The process is then followed by a photoresist developing process, during which the photoresist layer 50 is removed except at the injection passage 52, which stays on top of the injection orifice 48. This is shown in Figure 1J.

An orifice plate 54 is then formed by a nickel electroplating process, as shown in Figure 1K. The residual, second thick photoresist layer 50 in the injection passage 52 is then removed to form the injection passage in fluid communication with the ink chamber 40, as shown in Figure 1L. The photoresist removal process is performed by a wet etching technique.

The backside of the silicon substrate 10 is then etched by a reactive ion etching technique to remove the bottom insulating material layer 18, as shown in Figure 1M, and the top insulating material layer 16 exposed in the manifold 20.

In the final step of the process, as shown in Figure 1N, the first thick photoresist layer 38 is removed by a developing solution to vacate the ink chamber 40 in fluid communication with the manifold 20 and the injection passage 52. The present invention novel thermal bubble inkjet head that is equipped with symmetrical heaters is thus completed.

The operation of the present invention thermal bubble inkjet head having an off-shooter arrangement is shown in Figures 2A-2E. At the beginning of the process, the funnel-shaped manifold 20 and the ink chamber 40 are filled with an ink material. The ring-shaped heater electrode 28 is then heated to produce a ring-shaped bubble 70. As a result, a small ink column 74 is pushed out of the ink passageway 52 through the orifice 48. The bubble 70 enlarges, as shown in Figures 2B and 2C, to further push the ink column 74 out of the inkjet passage 52, as the heater electrode 28 continuously heats the ink contained in the ink chamber 40.

Finally, as shown in Figures 2D and 2E, the ring-shaped bubble 70 forms a circular bubble 76 and thus, cutting off the ink droplet 74 completely from the ink contained in the ink chamber 40. As a result, the inkjet droplet 74 separates from the inkjet

passageway 52 and forms an ink droplet toward the target. After the inkjet droplet 74 departs from the inkjet head 10, the bubble 76 collapses forming a void (not shown).

In a third preferred embodiment of the present invention, shown in Figure 3, a present invention thermal bubble inkjet head 64 is provided which has a different construction of the heater electrodes 66 and 68.

The present invention novel thermal bubble inkjet head equipped with symmetrical heaters and a method for fabricating the head have therefore been amply described in the above description and in the appended drawings of Figures 1A~3E.

While the present invention has been described in an illustrative manner, it should be understood that the terminology used is intended to be in a nature of words of description rather than of limitation.

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outsolved furthermore, while the present invention has been described in terms of a preferred embodiment, it is to be appreciated that those skilled in the art will readily apply these teachings to other possible variations of the inventions.

The embodiment of the invention in which an exclusive property or privilege is claimed are defined as follows.